

OPERATING SYSTEMS AND SYSTEMS INTEGRATION

Memory Management Tutorial — Solutions



3. A computer system has sixteen kilobyte pages and a 64-bit address bus, and 64-bit virtual addresses. In both the page table, and the page directory, entries are eight bytes in size. The system is shown in figure 1 on the next page. The following five parts of this question refer to this computer system.

Solutions



Figure 1: A method of paging.

(a) Calculate the number of bits required for the offset part of the virtual address, showing your working.

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- (b) Calculate the number of page table entries that are required.
- (c) If the page directory and page table are as nearly the same size as possible, determine the amount of RAM required to hold each one.
- (d) Briefly list one major problem with the paging scheme shown in figure 1 when used with this computer system.
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- (e) Explain with the aid of a diagram an alternative paging method that overcomes the problem you listed in the previous part 3d. Show how your design overcomes the problem.

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Solutions





Figure 2: A method of paging.

- **5.** A computer system has eight kilobyte pages and a 64-bit address bus. The following three parts of this question refer to this computer system.
 - (a) Calculate the number of page table entries that are required.



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